

FILE COPY

Modified 9-98

PTO/SB/21 (12-97)

Approved for use through 9/30/00. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Please type a plus sign (+) inside this box → **+**

pbs

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

BOX: Office of Initial Patent Examination

Express Mail Receipt No.

Application / Conf. No.	10/086,129 / 2232
Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Examiner Name	Unknown
Group Art Unit	2133
Issue Fee Batch No.	JUL 22 2002
Attorney Docket Number	X-1069 Technology Center 2100

RECEIVED

JUL 22 2002

Technology Center 2100

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet)	<input type="checkbox"/> After Allowance Communication to
<input type="checkbox"/> Amendment / Response	<input type="checkbox"/> Declaration / Oath	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Preliminary Amendment	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Affidavit(s)/declaration(s)	<input type="checkbox"/> Petition -	<input checked="" type="checkbox"/> Return Receipt Postcard
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> To Convert a Provisional Application	<input checked="" type="checkbox"/> Additional Enclosure(s) (please identify below):
<input type="checkbox"/> Change Status to LARGE ENTITY	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	Request for Corrected Filing Receipt; Supplemental Application Data Sheet
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	
<input type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> Request for Refund	
<input type="checkbox"/> Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A)		
<input type="checkbox"/> Certified Copy of Priority Document(s)		
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number	24309	(Customer Number)	Reg. Number 35,477
Attn:	H. C. Chan		
Signature			
Date	June 24, 2002	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040	

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D. C. 20231 on this date: June 24, 2002

Typed or Printed Name	PAT SLABACK		
Signature		Date	June 24, 2002

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.



IN THE UNITED STATES PATENT OFFICE

Applicant: David P. Schultz

Assignee: Xilinx, Inc.

Title: "Method and System for Flexibly JTAG TAP
Controllers for FPGA-Based System-On-Chip (SoC)"

Serial No.: 10/086,129 File Date: 02-28-02

Examiner: Unknown Art Unit: 2133

Docket No.: X-1069 US

COMMISSIONER FOR PATENTS
Office of Initial Patent Examination's
Customer Service Center
Washington, D. C. 20231

RECEIVED

JUL 22 2002

Technology Center 2100

REQUEST FOR CORRECTED FILING RECEIPT

Dear Sir:

Applicant requests a corrected Filing Receipt in the above identified patent application, which includes the following changes:

Please correct the title of the patent application to read: "Method and System for Flexibly Nesting JTAG TAP Controllers for FPGA-Based System-On-Chip (SoC)".

A red-lined copy of the Filing Receipt is enclosed for clarification. Also enclosed is a Supplemental Application Data Sheet which also corrects the title.

No fee is due with this communication.

The Commissioner is hereby authorized to charge any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

H. C. Chan
Attorney for Applicant
Reg. No. 35,477

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D. C. 20231, on June 24, 2002.

Pat Slaback
Name

Signature



JUL 01 2002
PATENT & TRADEMARK OFFICE
UNITED STATES GOVERNMENT

UNITED STATES PATENT AND TRADEMARK OFFICE

DOCKETED
APR 09 2002
COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/086,129	02/28/2002	2133	908	X-1069 US	3	20	5

CONFIRMATION NO. 2232

24309
XILINX, INC
 ATTN: LEGAL DEPARTMENT
 2100 LOGIC DR
 SAN JOSE, CA 95124

FILING RECEIPT



OC000000007802144

Date Mailed: 04/05/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

David P. Schultz, San Jose, CA;

RECEIVED

JUL 22 2002

Assignment For Published Patent Application

Xilinx, Inc., San Jose, CA;

Technology Center 2100

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 04/04/2002

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

flexibly
 Method and system for flexible nesting JTAG TAP controllers for FPGA-based system-on-chip
 (SoC)

RECEIVED
APR 09 2002
INTELL. PROP. DEPT.
XILINX, INC.

Preliminary Class